



8 bit to 16 bit EPROM programming adapter.
(c) Gary Lindsay 2018

Address Line	P1	U1
A0	12	10
A1	11	9
A2	10	8
A3	9	7
A4	8	6
A5	7	5
A6	6	4
A7	5	3
A8	27	41
A9	26	40
A10	23	39
A11	25	38
A12	4	37
A13	28	36
A14	29	35
A15	3	34
A16	2	33
A17	30	2
A18	31	1

Data Line	P1	U1
O0	13	14/15 (O0/O8)
O1	14	16/17 (O1/O9)
O2	15	18/19 (O2/O10)
O3	17	20/21 (O3/O11)
O4	18	23/24 (O4/O12)
O5	19	25/26 (O5/O13)
O6	20	27/28 (O6/O14)
O7	21	29/30 (O7/O15A-1)

Power / Control Line	P1	U1
VCC	32	22
GND	16	12, 31
VPP	1	32
CE	22	11
OE	24	13
N.C.		42

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Schematic
Drawn 17/2/2018